


```
0100: mov ax, 13h      0108: mov ds, bx      010E: inc ax          0113: inc si          011A: mov si, cx      01010100010010010101000001010001010
0103: int 10h         010A: xor cx, cx      010F: add al, ah      0114: cmp si, 0FA00h  011C: jmp short loc_10C 00101001011010101101000100111010100
0105: mov bx, 0A000h  010C: mov al, [si]    0111: mov [si], al    0118: jnz short loc_10C 01010110100010101010111001010110101010101001011110101010
0111110001011010101110010100101000010000000010100101011110010000110010101010010111001111100001101010000000111101001011100101010001
010011000010100001011110001010100101111001010100011011110010100101111001010001001010101010100000010101000001111010101010101
```

-

```

0100: mov ax, 13h      0108: mov ds, bx      010E: inc ax          0113: inc si          011A: mov si, cx      01010100010010010101000001010001010
0103: int 10h          010A: xor cx, cx      010F: add al, ah      0114: cmp si, 0FA00h   011C: jmp short loc_10C 00101001011010101101000100111010100
0105: mov bx, 0A000h   010C: mov al, [si]    0111: mov [si], al     0118: jnz short loc_10C 0101011010001010101011100101011010101010100110110101010
01111100010110101011100101001011110010100000000010101010101110011100011001010101001110011111000011010100000001111010010111100101010001
0100110001010000101111000101001011110010101000110010100010001111110010100101111001010100010010101010100010010101010000000101010000011110100101110010101010

```

Control Flow Graphs (CFGs)

– Functions can also be visualized as graphs

- Basic blocks = nodes
- Branches = edges

```
00000010 sub_00000010
00000010 push ebp
00000011 mov ebp, esp
00000013 sub esp, 128h
...
00000025 jz 00000050
0000002B mov eax, 0Ah
00000030 mov ebx, 0Ah
...
00000050 xor eax, eax
00000052 xor ebx, ebx
...
```

```
00000010 sub_00000010
00000010 push ebp
00000011 mov ebp, esp
00000013 sub esp, 128h
...
00000025 jz 00000050
```

```
0000002B
0000002B mov eax, 0Ah
00000030 mov ebx, 0Ah
...
```

```
00000050
00000050 xor eax, eax
00000052 xor ebx, ebx
...
```

- IDA also supports this type of visualization
- Useful for easy viewing of execution paths
- pGRAPH

Background Information

```
0100: mov ax, 13h      0108: mov ds, bx      010E: inc ax      0113: inc si      011A: mov si, cx      01010100010010010101000001010001010
0103: int 10h           010A: xor cx, cx      010F: add al, ah      0114: cmp si, 0FA00h    011C: jmp short loc_10C 0010100101101010101000100111010100
0105: mov bx, 0A000h    010C: mov al, [si]    0111: mov [si], al  0118: jnz short loc_10C 01010110100010101010111001010101001011100111110000110101010000001111010010111001010001
01001100010110010111001010100101110010100010000000010100101011100110000110010101010010111001111100001101010100101100111110000110101010101000000111101010111001010001
01001100010100001011110001010100101110010101000110010101000100011011110010100101111001010100010010101010101000000101010100000111110101010101001
```

- Input tracing
 - What code handles our inputs?
- Code coverage
 - How we can we determine where our fuzzer has gone?
 - How can we get our fuzzer deeper into the process?
- Complexity
 - How can we digest/understand mass volumes of machine code?
- Filtering
 - How can we filter uninteresting trace data? (Example: GUI handling code)
- Trace speed
 - How can we increase the speed of our tracing?

Background Information

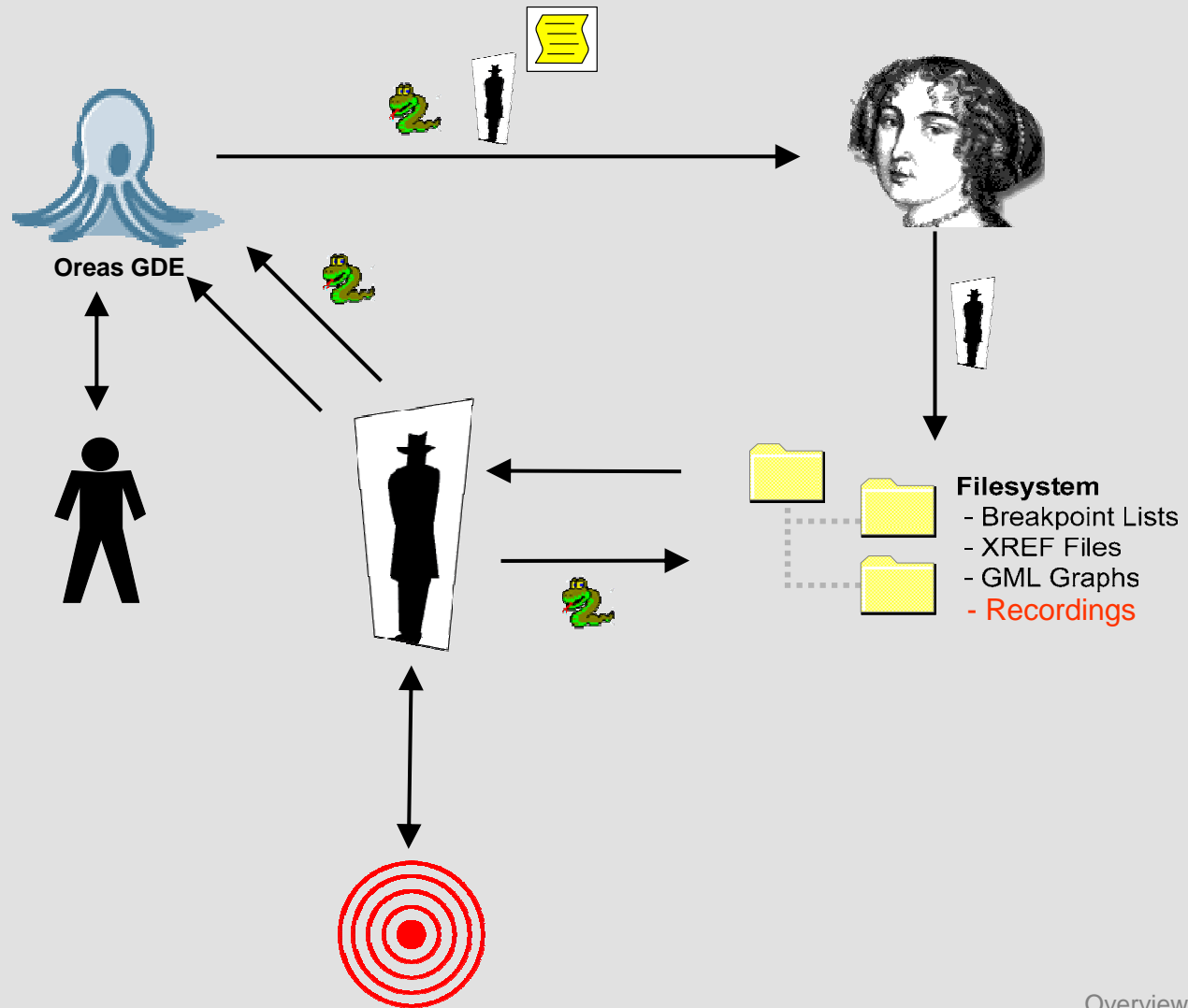
```
0100: mov ax, 13h      0108: mov ds, bx      010E: inc ax          0113: inc si          011A: mov si, cx      01010100010010010101000001010001010
0103: int 10h           010A: xor cx, cx        010F: add al, ah      0114: cmp si, 0FA00h   011C: jmp short loc_10C 00101001011010101101000100111010100
0105: mov bx, 0A000h    010C: mov al, [si]      0111: mov [si], al    0118: jnz short loc_10C 0101011010001010101110010101010101010101001011110101010
011111100010110101011100110010111001010100001000000000101001010101111001100001100101010100101110011111100001101010100000011111010010111100101010001
0100110001010000101111000101010010111001010100011001010100010001110111100101001011110010101000100101010101010100000010101010000111110101010101010
```

- Requirements
 - IDA Pro (commercial)
 - Python (free)
 - Oreas GDE Community Edition (free)
 - Quick demo in a second
- Components
 - IDA plug-in
 - Standalone tracer
 - Python scripts
- Development
 - C/C++
 - Python + custom API
 - Function Analyzer / Dumbug
- Related work
 - Sabre Security, BinNavi
 - HBGary, Inspector
 - SISecure? (Rootkit.com screenshot)

```
0100: mov ax, 13h      0108: mov ds, bx      010E: inc ax      0113: inc si      011A: mov si, cx      01010100010010010101000001010001010
0103: int 10h          010A: xor cx, cx      010F: add al, ah      0114: cmp si, 0FA00h      011C: jmp short loc_10C      00101001011010101101000100111010100
0105: mov bx, 0A000h    010C: mov al, [si]    0111: mov [si], al    0118: jnz short loc_10C      010101101000101010111001010110101010101001011110101010
01111110001011010111001010010101000010000000010100101011100110000110010101010011100111111000011010101000000111110100101111001010001
01001100010100001011110001010100101110010101000110010101000100011101111001010010101010101010101010101000000101010100001111101010101010
```

Data Flow Diagram

- Load binary in IDA
- Export to FS
- Stalk process
- Record
- Process results
- View in GDE
- Instrument graphs
- View in GDE again
- Make edits
- Mark locations
- Export back to IDA



```
0100: mov ax, 13h      0108: mov ds, bx      010E: inc ax      0113: inc si      011A: mov si, cx      01010100010010010101000001010001010
0103: int 10h         010A: xor cx, cx      010F: add al, ah      0114: cmp si, 0FA00h      011C: jmp short loc_10C      0010100101101010101000100111010100
0105: mov bx, 0A000h   010C: mov al, [si]      0111: mov [si], al      0118: jnz short loc_10C      01010110100010101010111001010101010100101110101010
011111000101101010111001100101010001010010101011100110000110010101001011100111111000011001010000001111010010111001010001
010011000101000010111100010101001011100101000110010101000100011101110010100101111001010100010010101010101000000101010100001111101010101001
```

- Built on top of Function Analyzer
- Analysis routine is applied to each identified function
- Breakpoint entries are generated for every node:
 - ndmpsrvr.dll:0002b1b0:0002b29c
 - Module, function offset, node offset
- Cross reference entries are generated for every call:
 - 0002cbd0:0002cc34:0002bb20
 - Function offset, node offset, called function offset
- Customized .GML graph's are generated for each function:
 - ndmpsrvr.dll-010a1af0.gml
 - ndmpsrvr.dll-010a1b20.gml

```
0100: mov ax, 13h      0108: mov ds, bx      010E: inc ax      0113: inc si      011A: mov si, cx      01010100010010010101000001010001010
0103: int 10h          010A: xor cx, cx      010F: add al, ah      0114: cmp si, 0FA00h      011C: jmp short loc_10C      00101001011010101101000100111010100
0105: mov bx, 0A000h    010C: mov al, [si]      0111: mov [si], al      0118: jnz short loc_10C      01010110100010101010111001010110101010100101110101010
011111000101101010111001100101010001000000001010010101011100110000110010101000101110011111100001101010100000011111010010111001010001
0100110001010000101111000101010010111001010100011001010100010001110111100101001011110010101000100101010101010000001010101000001111101010101001
```

Process Stalker Tracer Internals

- Built on top of Dmbug
- Attach to or load a target process
- On DLL load events
 - Determine module base address
 - Add loaded module to linked list
 - Automatically import available breakpoints
- On breakpoint events
 - If recording, write entry to file:
 - 0008c29d:000005cc:IMComms.dll:10001000:0000d25d
 - GetTickCount(), thread ID, module, module base, breakpoint offset
 - Optionally raise breakpoint restore flag and SINGLE_STEP
 - Optionally apply and record register inspection (next slide)

```
0100: mov ax, 13h      0108: mov ds, bx      010E: inc ax      0113: inc si      011A: mov si, cx      01010100010010010101000001010001010
0103: int 10h          010A: xor cx, cx      010F: add al, ah      0114: cmp si, 0FA00h    011C: jmp short loc_10C  00101001011010101101000100111010100
0105: mov bx, 0A000h    010C: mov al, [si]    0111: mov [si], al  0118: jnz short loc_10C  01010110100010101010111001010110101010101001011110101010
01111100010110101011100110001010100001000000000101001010101110011000011001010100101110011111100001101010100000011111010010111100101010001
0100110001010000101111000101010010111001010100011001010100010001110111100101001010101010101010101010100000010101010000111110101010101010
```

Process Stalker Tracer Register Inspection

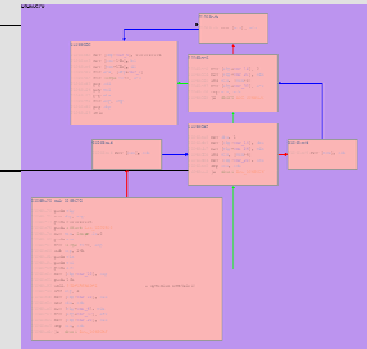
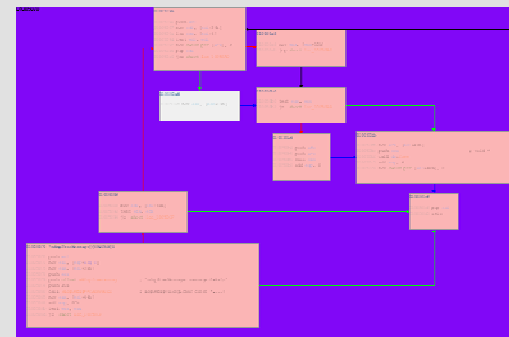
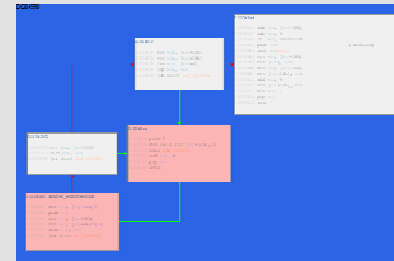
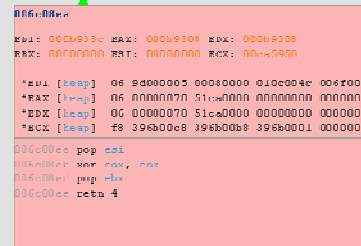
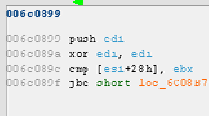
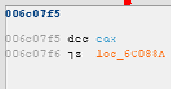
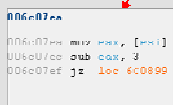
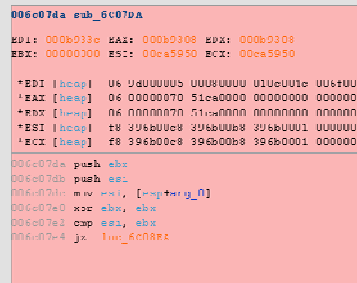
- Newer, unique and very useful feature
- Adds “smart” data about register contents to hit nodes
- Decreases performance of course
 - But not enough to outweigh the benefits
- Overview
 - On breakpoint event
 - For each register
 - Dereference as memory address
 - Ignore non-writeable addresses
 - If address is within stack range, mark stack, otherwise mark heap
 - Check for Unicode string
 - Check for ANSI string
 - Grab hex bytes (32)
 - Record

```
0100: mov ax, 13h      0108: mov ds, bx      010E: inc ax          0113: inc si          011A: mov si, cx      01010100010010010101000001010001010
0103: int 10h           010A: xor cx, cx      010F: add al, ah      0114: cmp si, 0FA00h   011C: jmp short loc_10C 00101001011010101101000100111010100
0105: mov bx, 0A000h    010C: mov al, [si]    0111: mov [si], al    0118: jnz short loc_10C 010101101000101010111001010110101010101001011110101010
01111100010110101011100110001010100001000000000101001010101111001100001100101010100101110011111100001101010100000011111010010111100101010001
010011000101000010111100010101001011110010101000110010101000100011101111001010010111100101010001001010101010101000000101010100001111101010101010
```

- Written in Python
- Process Stalker API: gml, ps_parsers
 - GML: Can parse and manipulate generated .GML files
 - PS_PARSERS: Can parse and manipulate breakpoint lists, recordings, cross-reference lists and register metadata files
 - Fully documented
- Various functionality already implemented:
 - Recording -> list -> breakpoint filter
 - Graph concatenation with optional cross referencing
 - Recursive graph visualization
 - Run trace “folding” for loop visualization (more on this later)
 - And more...

Now the pretty slides...

- Immediately see which nodes handle your input
- View graphs with different layout algorithms
- View relevant register data



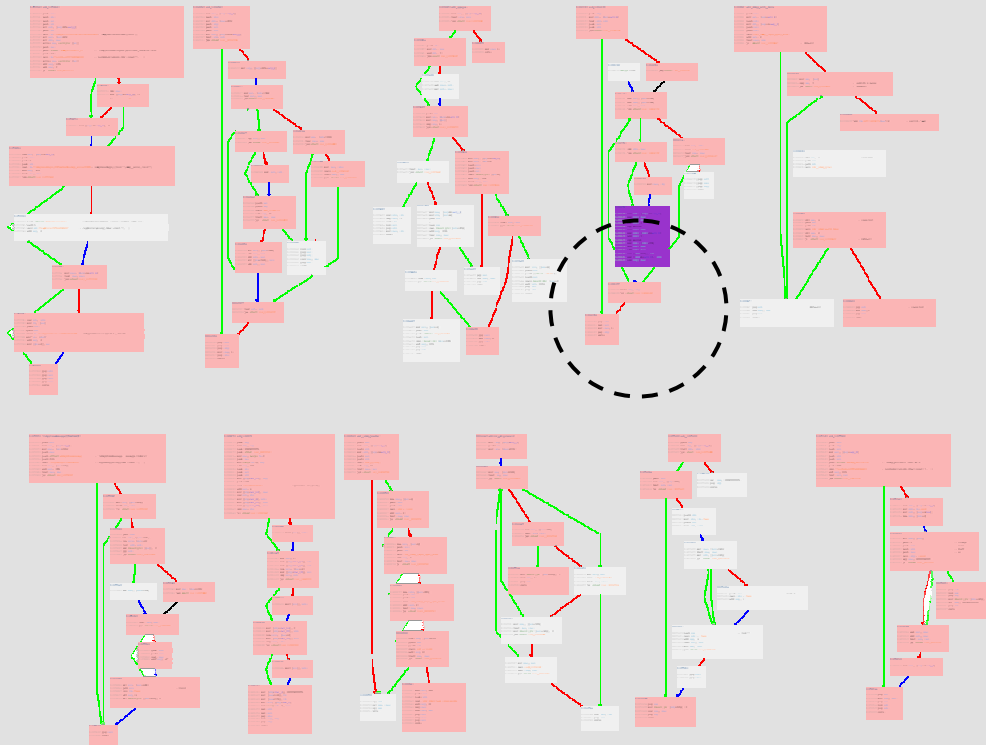
Hierarchical layout

Cluster orthogonal layout

Features and Benefits

Automated Highlighting

- Potentially interesting nodes are automatically highlighted
- ex: reps, *str*, *wcs*, *alloc*, *mem*



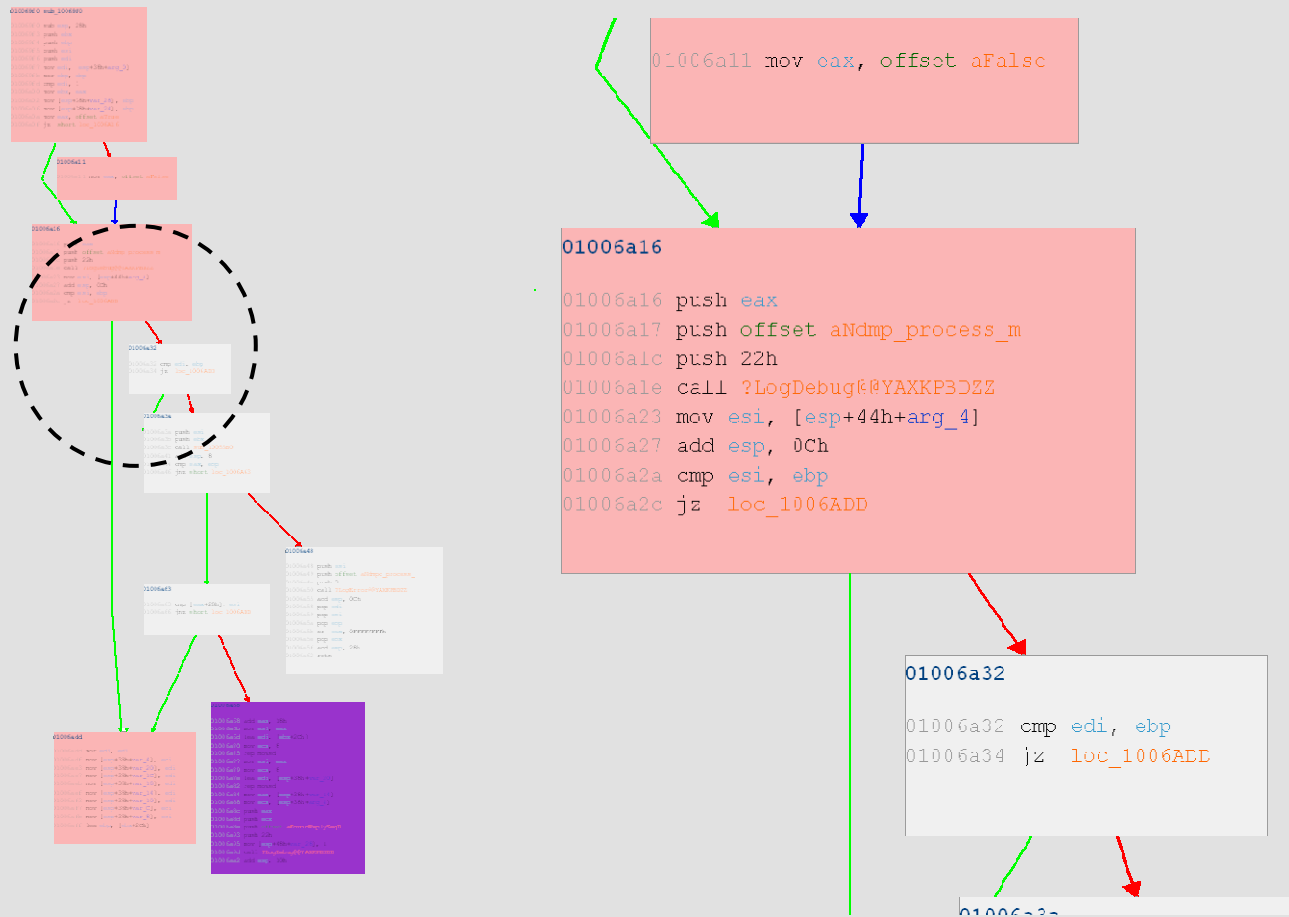
```
0100415f
0100415f mov edi, [esp+8+arg_0]
01004163 mov ecx, eax
01004165 mov edx, ecx
01004167 shr ecx, 2
0100416a rep movsd
0100416c mov ecx, edx
0100416e mov edx, [esp+8+arg_0]
01004172 and ecx, 3
01004175 rep movsb
01004177 mov esi, [ebp+2Ch]
0100417a add esi, eax
0100417c add edx, eax
0100417e mov [ebp+2Ch], esi
01004181 mov [esp+8+arg_0], edx
0100418b sub ebp, eax
```

```
01004187
01004187 test ebp, ebp
```

Features and Benefits

```
0100: mov ax, 13h      0108: mov ds, bx      010E: inc ax          0113: inc si          011A: mov si, cx      01010100010010010101000001010001010
0103: int 10h           010A: xor cx, cx      010F: add al, ah      0114: cmp si, 0FA00h  011C: jmp short loc_10C 01010100101101010101000100111010100
0105: mov bx, 0A000h    010C: mov al, [si]    0111: mov [si], al    0118: jnz short loc_10C 01010110100010101011001110000110010101010010111110000111101001011001010001
010011000101000010111100010101001011100101010001100101010001101111001010010111100101010010010101010101000000101010100001111101010101010
```

- Easily view and examine branch conditions
- Determine changes required to get fuzzer “deeper” into process state



Features and Benefits

```

0100: mov ax, 13h      0108: mov ds, bx      010E: inc ax          0113: inc si          011A: mov si, cx      01010100010010010101000001010001010
0103: int 10h          010A: xor cx, cx      010F: add al, ah      0114: cmp si, 0FA00h  011C: jmp short loc_10C 00101001011010101101000100111010100
0105: mov bx, 0A000h   010C: mov al, [si]    0111: mov [si], al    0118: jnz short loc_10C 0101011010001010101011100101011010101010100110110101010
0111110001011010101110010100101111001010000000001010101010111001110001100101010100111001111100001101010000001111010010111100101010001
010011000101000010111100010100101111001010100011001010001000111111001010010111100101010001001010101010001001010101000000101010000011110100101110010101010

```

Folding for Loop Visualization

- Algorithms exist for detecting logical loops statically
- ps_view_recording_trace applies a basic sequence folding routine to cluster repeat sequences
- Using a sliding window the input sequence is traversed. The longest possible discovered sequences are added to a cluster
- Viewing with cluster orthogonal generates a hideous graph. However, the logical loops are easy to spot and analyze
- Quick demo

```

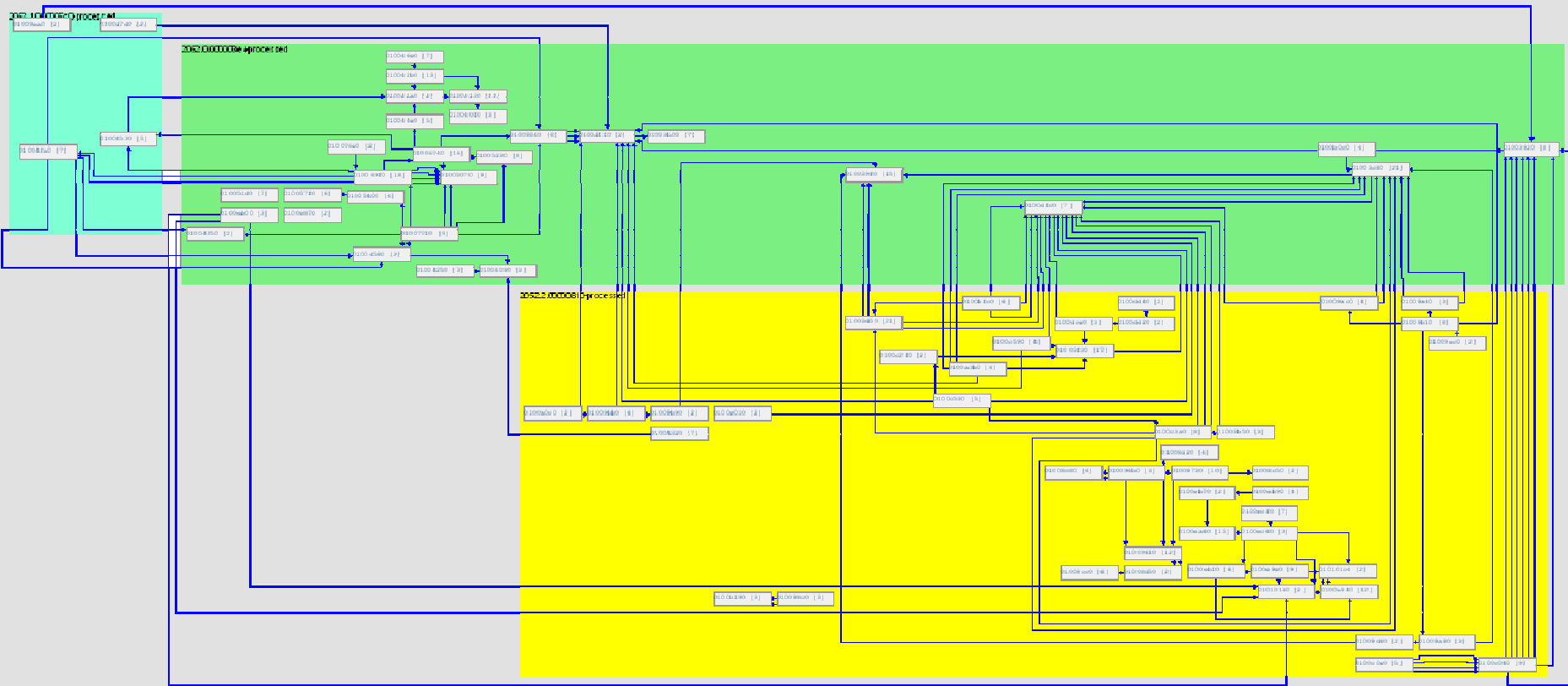
0100: mov ax, 13h      0108: mov ds, bx      010E: inc ax          0113: inc si          011A: mov si, cx      01010100010010010101000001010001010
0103: int 10h           010A: xor cx, cx        010F: add al, ah       0114: cmp si, 0FA00h   011C: jmp short loc_10C 0010100101101010101000100111010100
0105: mov bx, 0A000h    010C: mov al, [si]      0111: mov [si], al     0118: jnz short loc_10C 010101101000101010101110010101010101010100101110101010
01111100010110101011100110010111001010100001000000000101001010101111001100001100101010100101110011111100001101010100000011111010010111100101010001
01001100010100001011110001010100101110010101000110010101000100011101111001010010111100101010001001010101010101000000101010100001111101010101010

```

- Much faster than single-step tracing
- Two modes of operation
 - Breakpoint restore
 - One shot
- Breakpoint filtering can further improve performance
 - Functions only
 - Potentially interesting modules only

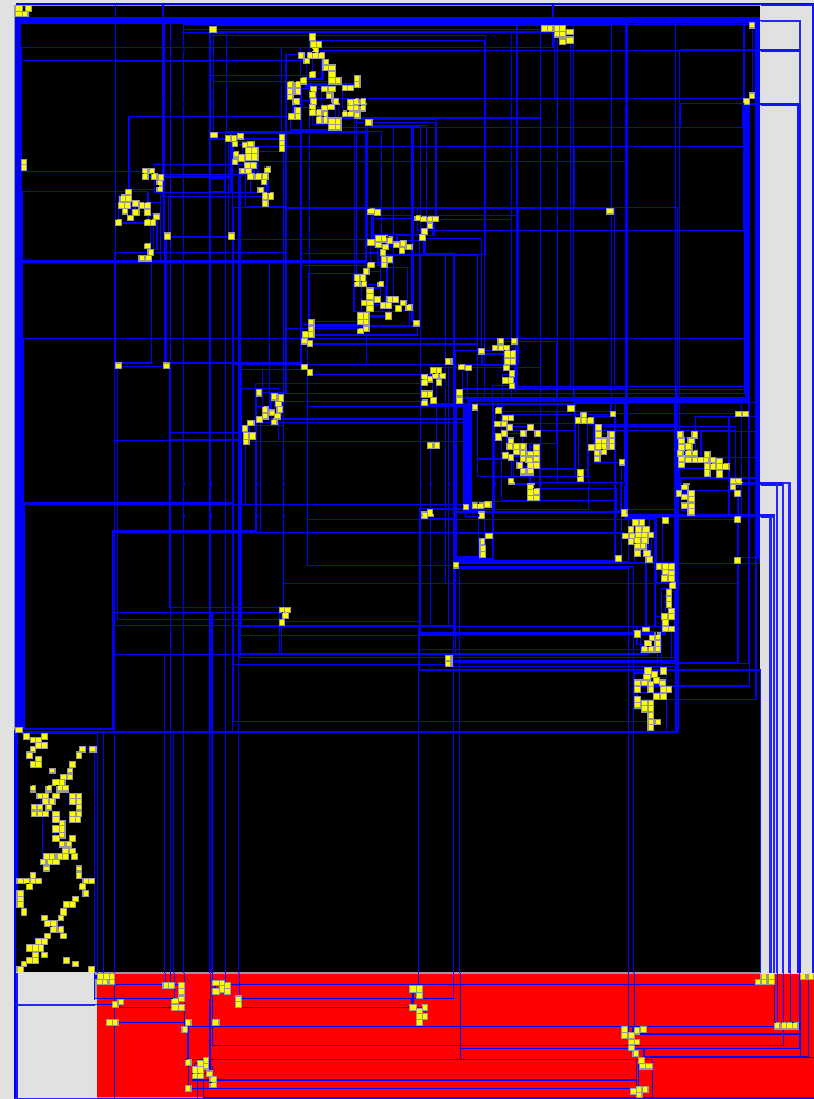
```
0100: mov ax, 13h      0108: mov ds, bx      010E: inc ax      0113: inc si      011A: mov si, cx      01010100010010010101000001010001010
0103: int 10h          010A: xor cx, cx      010F: add al, ah      0114: cmp si, 0FA00h      011C: jmp short loc_10C  00101001011010101101000100111010100
0105: mov bx, 0A000h    010C: mov al, [si]      0111: mov [si], al      0118: jnz short loc_10C  010101101000101010111001010110101010101001011110101010
011111100010110101011100110010111100101010000100000000010100101010111100110000110010101010011100111111000011010101000000111110100101111001010001
010011000101000010111100010101001011110010101000110010101000100011101111001010010111100101010001001010101010101000000101010100000111110101010101010
```

- ex: Authenticated vs. non-authenticated code
- ex: What our fuzzer has reached vs. what our fuzzer can reach



Features and Benefits

- Recordings can be joined and/or diffed
- Example: GUI handling code can be recorded and diffed out
- MS05-030: MSOE.DLL
 - Black: GUI functions
 - Red: Non-GUI functions
- The graph on the right was generated using state mapping



```
0100: mov ax, 13h      0108: mov ds, bx      010E: inc ax      0113: inc si      011A: mov si, cx      01010100010010010101000001010001010
0103: int 10h          010A: xor cx, cx      010F: add al, ah      0114: cmp si, 0FA00h    011C: jmp short loc_10C  0010100101101010101000100111010100
0105: mov bx, 0A000h    010C: mov al, [si]    0111: mov [si], al  0118: jnz short loc_10C  0101011010001010101011100101010101010100101110101010
011111000101101010111001100101110010101000010000000001010010101011110011000011001010101001011100111110000110101010000001111010010111100101010001
0100110001010000010111100010101001011100101010001100101010001000110111100101001011110010101000100101010101010100000010101010000011111010101010100
```

- Node hit counts
- Node transition times

```
$ ps_view_recording_stats 2284.0.000003d8-processed
```

```
function block hit counts for module irc.dll
```

| | | | | | |
|----------|---|----------|---|----------|----|
| 46011500 | 5 | 46014e81 | 1 | 46012510 | 4 |
| 4600b010 | 2 | 460179e0 | 1 | 4600ae70 | 4 |
| 4601559e | 1 | 46006820 | 4 | 46006630 | 24 |
| ... | | | | | |

```
function transition times (milliseconds) for module irc.dll
```

| | | | | | |
|----------|----|----------|----|----------|----|
| 4600f560 | 40 | 460067e0 | 0 | 4600f560 | 0 |
| 46006820 | 0 | 46006630 | 0 | 4601559e | 0 |
| 46006630 | 21 | 4600f560 | 60 | 460067e0 | 10 |
| 4600f560 | 0 | 46001690 | 0 | 4600f560 | 0 |
| ... | | | | | |

Demonstration

```

0100: mov ax, 13h      0108: mov ds, bx      010E: inc ax          0113: inc si          011A: mov si, cx      01010100010010010101000001010001010
0103: int 10h          010A: xor cx, cx        010F: add al, ah      0114: cmp si, 0FA00h   011C: jmp short loc_10C 00101001011010101101000100111010100
0105: mov bx, 0A000h    010C: mov al, [si]      0111: mov [si], al    0118: jnz short loc_10C 01010110100010101011100101010101010101001011110101010
01111110001011010101110011001011110010101000010000000001010010101011110011000011001010101001011100111111000011010101000000111110100101111001010001
0100110001010000101111000101010010111100101010001100101010001000111011110010100101111001010100010010101010101010000001010101000011111010101010100

```

TippingPoint
a division of 3Com

Demonstration

```

0100: mov ax, 13h      0108: mov ds, bx      010E: inc ax          0113: inc si          011A: mov si, cx      01010100010010010101000001010001010
0103: int 10h          010A: xor cx, cx      010F: add al, ah      0114: cmp si, 0FA00h  011C: jmp short loc_10C 00101001011010101101000100111010100
0105: mov bx, 0A000h   010C: mov al, [si]    0111: mov [si], al    0118: jnz short loc_10C 01010110100010101010111001010110101010100101110101010
0111110001011010101110010100010000000101001010111001100001001010101011100111110000101010000001111010010111001010001
01001100010100001011110001010100101110010100011001010001000110111100101001011110010100010010010101010000001010100000111101010101010

```

```
$ ps_process_recording gui_shit

$ cat gui_shit.* > gui_shit.processed

$ wc -l gui_shit.processed
4455 gui_shit.processed

$ time ps_bp_filter msoe.dll.bpl msoe.dll.nogui \
`ps_recording_to_list gui_shit.processed msoe.dll` out
real    0m28.367s

$ wc -l msoe.dll.bpl msoe.dll.nogui
58165 msoe.dll.bpl
50560 msoe.dll.nogui

$ time ps_view_recording_funcs 844.1.processed > hitgraph.gml
real    0m7.446s

$ time ps_graph_highlight -nodes hit hitgraph.gml > hitgraph_hl.gml
real    0m5.795s

$ time ps_add_register_metadata 844-regs.1 hitgraph_hl.gml > with_regs.gml
real    0m7.977s
```

Demonstration

Demonstration

- Still working on this stuff:
 - Argument dereferencing
 - With automatic detection of ASCII and Unicode strings
 - Smarter highlighting
 - PDB parsing for when you have source code (hit lines)
- Other ideas:
 - Arbitrary data structure visualization
 - Data flow visualization
- Potential design changes:
 - Remove dependency on IDA
 - Switch from debugger to emulation instrumentation (BOCHS)

```
0100: mov ax, 13h      0108: mov ds, bx      010E: inc ax      0113: inc si      011A: mov si, cx      01010100010010010101000001010001010
0103: int 10h          010A: xor cx, cx      010F: add al, ah      0114: cmp si, 0FA00h    011C: jmp short loc_10C  0010100101101010101000100111010100
0105: mov bx, 0A000h    010C: mov al, [si]    0111: mov [si], al  0118: jnz short loc_10C  010101101000101010101110010101010101010100101110101010
0111111000101101010111001100101110010101000010000000001010010101011100110000110010101010011100111111000011010101000000111110100101111001010001
010011000101000010111100010101001011100101010001100101010001000111011110010100101111001010100010010101010101010000001010101000001111101010101010
```

www.OpenRCE.org

Open Reverse Code Engineering Community Website

```

0100: mov ax, 13h      0108: mov ds, bx      010E: inc ax          0113: inc si          011A: mov si, cx      01010100010010010101000001010001010
0103: int 10h           010A: xor cx, cx       010F: add al, ah       0114: cmp si, 0FA00h   011C: jmp short loc_10C 00101001011010101101000100111010100
0105: mov bx, 0A000h    010C: mov al, [si]     0111: mov [si], al     0118: jnz short loc_10C 0101011010001010101110010101010101010100101110101010100101110101010
01111110001011010101110011001011110010101000010000000001010010101011110011000011001010101001011100111111000011010101000000111110100101111001010001
01001100010100001011110001010100101110010101000110010101000100011101111001010010111100101010001001010101010101000000101010100000111110101010101010

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